



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Michael H. Perrott
Title: METHOD AND APPARATUS FOR BIT ERROR RATE DETECTION
Application No.: 09/888,708 Filed: June 25, 2001
Examiner: Unknown Group Art Unit: 2133
Atty. Docket No.: 026-0015

March 3, 2003

COMMISSIONER FOR PATENTS
Washington, DC 20231

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UNDER 37 C.F.R. § 1.97**

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Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO-1449
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☒ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231.

Mark Zagorin
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3/3/03
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Respectfully submitted,

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